MODULATOR SYSTEMS FOR THE ESS 2.5 MeV FAST CHOPPER

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Abstract

Modulator system requirements for the ESS 2.5 MeV slow wave chopper are considered to be challenging. A description is given of candidate modulation schemes, and modulator configurations, where the chopper E-field is generated by a gated high frequency sinusoidal / trapezoidal waveform or by a combination of fast transition short duration, and slow transition long duration, trapezoidal pulses.

1 INTRODUCTION

The European Spallation Source (ESS) [1], is the most ambitious of the existing proposals for the next generation of accelerator driven pulsed neutron sources [2]. Designed to address the rapid expansion in the field of condensed matter research, the ESS accelerator will generate intense, fast pulsed, beams of neutrons, by delivering up to 5 MW of protons to a liquid mercury (4 MW), and solid tantalum (1 MW) target, at a pulse repetition frequency (prf) of 40 and 10 Hz, respectively.

2 ESS 2.5 MeV FAST CHOPPER



Figure 1: ESS block schematic

Components of the ESS, are shown in schematic form in figure 1. The low beam loss (< 1 nA/m @ 1.334 MeV) design features a high current H- linac, and twin accumulator rings with sequential charge exchange injection and sequential fast extraction.

The time dependant function of the fast choppers, switching, and fast extraction magnets, is shown in figure 2, where the 'history' of a beam pulse is traced from RFQ to target, over a linac cycle. The seven chopp--ing regimes listed in figure 2, are described in Table 1.

Stringent beam loss requirements dictate that the chopping field in the 2.5 MeV medium energy beam transport line (MEBT), rise and fall (1 to 90%, 90 to 1%, for < 2% transverse emittance growth) within the beam bunch interval of 2.9 ns. In addition, the chopping field duration must be instantaneously programmable in the range 240 ns - 0.1 ms (See Table 1). Slow wave (E-field) transmission line structures have demonstrated field



Figure 2: ESS beam chopping and switching

transition times in the nanosecond regime [3, 4,], but the requirement for a fast transition time, long pulse duration, high voltage (~2.5 kV), high burst frequency modulator, is challenging.

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Label	Chopping Regime	Dura-	Chopper Duty Cycle		No. of	Function
fig.2		-tion	On time	Off time	Turns*	
Α	Ion source transition	50µs	50 µs	0	60	Gates low intensity beam
В	Beam duty cycle	50µs	803.57 ns	0 to		Limits linac beam loading
	ramping	-	to 241.1 ns	562.5 ns	60	transient
С	Ring 1 stacking	0.6 ms	241.1 ns	562.5 ns	740	Gaps for fast extraction
D	Ring switching	0.1 ms	0.1 ms	0	120	Gap for ring switching
E	Beam duty cycle	50µs	803.57 ns	0 to		Limits linac beam loading
	ramping	-	to 241.1 ns	562.5 ns	60	transient
F	Ring 2 stacking	0.6 ms	241.1 ns	562.5 ns	740	Gaps for fast extraction
G	Ion source transition	50µs	50 µs	0	60	Gates low intensity beam
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Table 1: ESS chopping regimes

* One accumulator ring revolution period = 803.57 ns

3 CANDIDATE MODULATION SCHEMES

Two candidate modulation schemes are presented. Both schemes demonstrate a new 'periodic' transverse beam kicking technique that should find application in chopping and funnelling systems. The conflicting transit--ion time and pulse duration requirements are resolved by making use of the periodic nature of the beam.



Figure 3: Scheme A / Beam trajectories

Unipolar, long duration fields are replaced by bipolar (sinusoidal or trapezoidal pulsed) slow wave fields at half linac frequency, and the gated RF amplifier, bipolar pulse generator, and transmission line transformer, become the components of choice for modulator design.

3.1 Modulation scheme A

Dimensions, and beam trajectories for modulation scheme A are shown in figure 3, where the chopped and un-chopped beams are shown in red and green respectively. The beam trajectories are beam envelopes containing 100% of beam particles, and include the effects of space charge and image forces [6].

A timing and layout schematic for scheme A is shown in figure 4, where two slow wave electrodes (A, and B) produce a bipolar pulsed field at half linac frequency, that deflects alternate bunches through \pm 16.5 mr, to beam dump (BD1), for the total duration of the chopped beam interval. Each modulator must deliver a peak power of ~53 kW (1.65 kW mean) into a 50 Ohm load. BD1 must dissipate a total beam power of ~4.7 kW.

3.2 Modulation scheme B

Beam trajectories, and a timing schematic for scheme B are shown in figures 5, and 6 respectively, where slow wave chopper 1, produces a bipolar , pulsed field that deflects just two adjacent bunches through \pm 16.5 mr, to beam dumps 1 and 2 (BD1, BD2), creating two ~10 ns duration gaps in the bunch train, at the beginning and end of each chopped beam interval. Modulator peak power at ~35 kW (~33 W mean !), is much lower than scheme A.



Figure 4: Scheme A / Timing schematic



Figure 5: Scheme B / Beam trajectories(a) Fast transition pre-chopping - beam to BD1&2.(b) Slower transition chopping - beam to BD3.

The chopper 2, slow wave, lumped element array (6 pairs of 6 cm long electrodes), produces a unipolar, pulsed field that deflects the beam through 47 mr to a water cooled electrode / beam dump array (BD3).

The chopper 2 modulators (12 x switch modules) are limited to transition times of ~ 8 ns. Pre-chopping in chopper 1 ensures that no partially chopped bunches result from the slower field transition time of chopper 2. Each switch module will dissipate ~ 40 W mean, with chopper 1 electrodes, BD1, 2, and 3 dissipating beam powers of ~ 35 , ~ 100 , ~ 35 , and ~ 4530 W, respectively.



Figure 6: Scheme B / Timing schematic

4 MODULATOR DESIGN

A block schematic of the proposed fast chopper modulator system, for modulation scheme B, is shown in figure 7. Systems 1A and 1B drive chopper 1A and 1B distributed slow wave electrodes (see figure 4), and output fast transition (~2 ns), quasi-trapezoidal, bipolar high voltage pulses (\pm 1.4 kV) into a 50 ohm load. The modular configuration makes extensive use of high power transmission line transformers (TLT) [7] to impedance match, and combine the outputs from 32 solid state high voltage pulse generators.



Figure 7: Scheme B / Modulator block schematic

A block schematic of the pulse generator module is shown in figure 8. The class-D, push-pull, current switching design, outputs ~ \pm 245 V (peak) from a 52 V supply using eight RF power mosfets (150 W, 2 GHz f₁),



Figure 8: Scheme B / System 1 / Module



Figure 9: Scheme B / System 2 / Module

and a step up TLT. For scheme B, the module can be very compact, as the average duty cycle for system 1 is ~ 0.12 %, and mean load power per module is only ~ 1 W.

Systems 2A and 2B drive chopper 2A and 2B lumped element slow wave electrodes, and output ~8 ns transition, unipolar, trapezoidal pulses (+3.0 and -3.0 kV) into a 40 pF load. A block schematic of the module is shown in figure 9. The high voltage mosfet switch has the standard dc-coupled, totem-pole configuration. The 40 W modules will be close-coupled ($\langle \lambda/10 \rangle = \langle 50 \rangle$ cms) to individual electrodes to preserve signal integrity.

5 SUMMARY

A modulation scheme and a modulator design for the ESS 2.5 MeV fast beam chopper have been identified. The scheme makes use of a new modulation technique that enables the implementation of an elegant, low average power, modulator.

6 ACKNOWLEDGEMENT

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