# THE DC-MAGNET POWER SUPPLIES FOR THE LCLS INJECTOR\*

A. de Lira<sup>#</sup>, P. Bellomo, K. Luchini, and D. MacNair SLAC, Menlo Park, CA 94025, U.S.A.

#### Abstract

The injector section of the LCLS machine at SLAC [1] requires 100+ dc-magnet power supply systems for operation. Intermediate rack-mounted power supplies provide dipole, quadrupole and solenoid magnet power up to 20 kW at 375 A. A SLAC-developed Ethernet power supply controller interfaces to these commercially available power supplies. Smaller bipolar units rated 6A, 12A, and 30A provide power for small magnets such as correctors, dipoles, and quadrupoles. DACs and ADCs housed in VME crates control these power supplies. For all systems, stability requirements are better than 1000 ppm and EPICS is the controls interface. This paper describes the main hardware characteristics of the power supply systems.

### **INTRODUCTION**

The Power Conversion Department (PCD) [2] at SLAC, in close cooperation with the LCLS Controls group [3], was responsible for defining the major characteristics of the power supply (PS) systems for the LCLS injector: their concept, specification, procurement, installation, and commissioning.

PCD conducted regular reviews throughout the design, fabrication, and installation phases to account for the performance, integration, and safety aspects of each power system.

Successful commissioning of the dc-magnet power supplies occurred in early 2007 [4].

## **INTERMEDIATE POWER SUPPLIES**

Figure 1 shows the power supply's system architecture and Table 1 presents the basic performance requirements.

A 1U, 19-inch rack-mounted Ethernet-based power supply controller (EPSC) provides the precision regulation of magnet current via a closed external current loop. It also monitors power supply's current, voltage, and ground current. It manages the external interlocks, and communicates abnormal conditions to the LCLS control system.

The EPSC provides better then 1-ppm current setting resolution and 50-ppm stability over a 40 °C environment change. It employs a standard analog power supply interface to control standard commercial power supplies of many ratings, types and from different manufacturers.

The control loop of each power supply was adjusted to yield an overall output current regulation bandwidth of 10 Hz. Procedures to adjust this bandwidth are described in [5].

07 Accelerator Technology Main Systems



Figure 1: The Intermediate PS Architecture

Table 1: Intermediate PS - Performance Requirements

Output V, I and P	Up to 200 V and 375 A,
	10 kW/15 kW rack-mounted PS
Stability versus temp	$\leq$ 2 ppm / °C – 10 to 100%
Short/Long term	100 ppm RMS, 1 s / 10 s
stability	
BW as V/I source	dc to $-3 \text{ dB} \ge 1 \text{ kHz} / 10 \text{Hz}$
Conducted EMC	FCC, Part 15, Class A
Life / MTBF	$\geq$ 20 years / $\geq$ 100,000 hrs
Load	$0.05 \text{ H} \le L \le 1.0 \text{ H}$
	$0.1 \ s \le L/R \le 1.0 \ s$

## THE ETHERNET POWER SUPPLY CONTROLLER (EPSC)

The EPSC is a replacement for the PEP II Bitbus PS controller. It is interchangeable with the PEP II chassis, but uses an Ethernet instead of Bitbus protocol [6]. It provides greatly enhanced performance and diagnostics for approximately US\$2500 / unit. Table 2 presents the main performance characteristics of the EPSC, and figure 3 its internal structure.

The main features of the EPSC are:

- Daughter boards for magnet dependant configuration
- Support for redundant transductor
- Hardware protection, latching and reporting of all system faults by FPGA
- Ramping of power supply output current

Table 2: EPSC Performance	Characteristics
---------------------------	-----------------

Parameter	
DAC resolution	24 bits
DAC noise (0.1 to 10 Hz)	2 μVrms
DAC Linearity 0-10V	2 ppm max
ADC effective # of bits	20 bits
ADC readings per second	60
ADC noise (0.1 to 10 Hz)	3 μVrms
ADC temp stability max	0.25 ppm / °C max

<sup>\*</sup> This work was performed in support of the LCLS project at SLAC and funded by Department of Energy contract DE-AC02-76SF00515 # delira@slac.stanford.edu



Figure 2: internal structure of the EPSC

#### **CORRECTOR POWER SUPPLIES**

Figure 3 shows the general power system configuration for the smaller bipolar systems and Table 3 shows the performance requirements.

A programmable logic controller (PLC) controls and monitors the operation of the bulk power supplies. The PLC is EPICS-compatible. It provides remote turn-on and turn-off capability, and monitors the output voltages and currents of the three bulk power supplies. It also detects ground current flow.

A single Eurocard crate accommodates up to 16 bipolar power modules (MCOR) of different ratings [7]. A single bulk PS rated 60V and 165A provides regulated voltage to two crates.



Figure 3: Bulk PS System Architecture - Magnets < 30A

Table 3: Corrector PS uses and requirements

Ratings	40V, 6A or 40V, 12A or 40V,
	30A
Uses	Power 6 A, 12 A, and 30 A
	correctors, small quadrupoles,
	and trim magnets that require
	unipolar or bipolar current
Stability versus temp	$\leq$ 13 ppm / °C, 10 to 100%
Short term stability	30 ppm RMS, 1 s
Long term stability	400 ppm RMS, 10 s at 30 °C
Ambient	4 °C to 45 °C
Bandwidth as I source	dc to $-3dB \ge 10Hz$
Load	$0.05 \text{ H} \le L \le 1.0 \text{ H}$
	$0.1 \ s \le L/R \le 1.0 \ s$

#### SAFETY

The Power Conversion Department held regular safety and project review meetings throughout the program. An Electrical Equipment Inspection Program (EEIP) at SLAC maximized the use of nationally recognized testing laboratory (NRTL) listed components and ensured the equipment satisfies the NEC and OSHA safety codes [8].

Power supply to magnet power cables are flexible stranding, low smoke, zero halogen, construction suitable for cable tray and accelerator housing use. Cable ampacities conform to the 2005 NEC, Tables 310-16 and 310-17. Cable tray fill conforms to NEC Article 392.

## ACKNOWLEDGEMENTS

The authors wish to acknowledge the contributions of the administrative, technical and engineering staffs from the PCD and LCLS.

### REFERENCES

- [1] http://www-ssrl.slac.stanford.edu/lcls/
- [2] http://ped.slac.stanford.edu:8080/
- [3] http://www.slac.stanford.edu/grp/lcls/controls/
- [4] P. Emma et al, "Initial Commissioning Experience with the LCLS Injector", these proceedings.
- [5] P. Bellomo and A. de Lira, "SPEAR3 Intermediate Dc Magnet Power Supplies", EPAC'04, Lucerne, Switzerland, July 2004.
- [6] http://www.bitbus.org/faq.htm
- [7] G. E. Leyh et al, "A Multi-Channel Corrector Magnet Controller", PAC'95, Dallas, May 1995.
- [8] http://www-group.slac.stanford.edu/essg/eeip/